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- 1 [Lx: a technology platform for customizable VLIW embedded process](#)  
 Paolo Faraboschi, Geoffrey Brown, Joseph A. Fisher, Giuseppe Desoli, Fred June 2000 **ISCA '00**: Proceedings of the 27th annual international sympos  
**Publisher:** ACM

 Full text available: Pdf (344.41 KB) Additional Information: [full citation](#), [abstract](#), [re](#)
**Bibliometrics:** Downloads (6 Weeks): 9, Downloads (12 Months): 67, Citation (

Lx is a scalable and customizable VLIW processor technology platform d and STMicroelectronics that allows variations in instruction issue width, structures and the processor instruction set. For ...

Also published in:

 May 2000 **SIGARCH Computer Architecture News** Volume 28 Issue 2

- 2 [PipeRench: a co/processor for streaming multimedia acceleration](#)  
 Seth Copen Goldstein, Herman Schmit, Matthew Moe, Mihai Budiu, Srihari Ronald Laufer  
 May 1999 **ISCA '99**: Proceedings of the 26th annual international sympos  
**Publisher:** ACM

 Full text available: Publisher Site , Pdf (202.69 KB) Additional Information: [full citation](#), [index terr](#)
**Bibliometrics:** Downloads (6 Weeks): 8, Downloads (12 Months): 54, Citation (

Future computing workloads will emphasize an architecture's ability to p calculations on massive quantities of mixed-width data. This paper desc fabric architecture, PipeRench, optimized to accelerate ...

Also published in:

 May 1999 **SIGARCH Computer Architecture News** Volume 27 Issue 2

- 3 [Speeding up protocols for small messages](#)  
 Trevor Blackwell  
 August 1996 **SI GCOMM '96**: Conference proceedings on Applications, techn protocols for computer communications  
**Publisher:** ACM

 Full text available: Pdf (98.82 KB) Additional Information: [full citation](#), [abstract](#), [re](#)
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Many techniques have been discovered to improve performance of bulk use large messages. This paper describes a technique that improves protocols that use small messages, such as signalling protocols, ...

Also published in:

October 1996 **SIGCOMM Computer Communication Review** Volume 26 Issue

- 4 [MorphoSys: case study of a reconfigurable computing system target](#)  
 [Hartej Singh, Guangming Lu, Eliseu Filho, Rafael Maestre, Ming-Hau Lee, F. Bagherzadeh](#)

June 2000 **DAC '00**: Proceedings of the 37th conference on Design autom


**Publisher:** ACM

Full text available:  Pdf (968.27 KB) Additional Information: [full citation](#), [abstract](#), [re](#)


**Bibliometrics:** Downloads (6 Weeks): 17, Downloads (12 Months): 49, Citation

In this paper, we present a case study for the design, programming and system-on-chip, MorphoSys, which is targeted at computation-intensive transistor design combines a reconfigurable array of cells ...

**Keywords:** MPEG-2, SIMD, automatic target recognition, dynamic conf processors, scheduling

- 5 [Wireless protocols design: challenges and opportunities](#)  
 [J. L. da Silva, Jr., M. Sgroi, F. De Bernardinis, S. F. Li, A. Sangiovanni-Vinc](#)  
 May 2000 **CODES '00**: Proceedings of the eighth international workshop (


**Publisher:** ACM

Full text available:  Pdf (141.71 KB) Additional Information: [full citation](#), [abstract](#), [re](#)

**Bibliometrics:** Downloads (6 Weeks): 4, Downloads (12 Months): 30, Citation (

Modern wireless communication systems require the deployment of incr that satisfy tight requirements at low implementation cost, especially in consumption. Most protocol design methodologies currently ...

**Keywords:** case study, communication refinement, function/architectur design

- 6 [Hardware compilation for FPGA-based configurable computing mact](#)  
 [Xiaohan Zhu, Bill Lin](#)

June 1999 **DAC '99**: Proceedings of the 36th ACM/IEEE conference on Desi

**Publisher:** ACM

Full text available:  Pdf (129.13 KB) Additional Information: [full citation](#), [references](#), [cite](#)


**Bibliometrics:** Downloads (6 Weeks): 1, Downloads (12 Months): 14, Citation (

- 7 [Low power scalable encryption for wireless systems](#)

[James Goodman, Anantha P. Chandrakasan](#)

January 1998 **Wireless Networks**, Volume 4 Issue 1

**Publisher:** Kluwer Academic Publishers

Full text available:  Pdf (7.39 MB)



Additional Information: [full citation](#), [abstract](#), [re](#)

**Bibliometrics:** Downloads (6 Weeks): 15, Downloads (12 Months): 107, Citation (

Secure transmission of multimedia information (e.g., voice, video, data, wireless network applications. Wireless transmission imposes constraint systems such as low power consumption, tolerance to ...

- 8 [High-level scheduling model and control synthesis for a broad range](#)  
Chih-Tung Chen, Kayhan Küçükçakar  
November 1997 **ICCAD '97: Proceedings of the 1997 IEEE/ACM international**  
aided design

**Publisher:** IEEE Computer Society


Full text available:  Publisher Site ,  Pdf (110.32 KB) Additional Information: [full citation](#)  
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**Bibliometrics:** Downloads (6 Weeks): 3, Downloads (12 Months): 18, Citation (

This paper presents a versatile scheduling model and an efficient control enables architectural (high-level) design/synthesis systems to seamless architectural design applications from datapath-dominated ...

**Keywords:** control synthesis, scheduling model, multi-phase clocking, relative scheduling, high-level synthesis, architectural synthesis, behavior power optimization.

- 9 [Functional partitioning improvements over structural partitioning for synthesis: tool performance](#)

 Frank Vahid, Thuy-Dm Le, Yu-Chin Hsu

April 1998 **Transactions on Design Automation of Electronic Systems**

**Publisher:** ACM


Full text available:  Pdf (225.74 KB) Additional Information: [full citation](#), [abstract](#), [re](#)

**Bibliometrics:** Downloads (6 Weeks): 1, Downloads (12 Months): 27, Citation (

Incorporating functional partitioning into a synthesis methodology leads advantages. In functional partitioning, we first partition a functional specification into subspecifications and then synthesize structure for each, ...

**Keywords:** behavioral synthesis, functional partitioning, system-level synthesis

- 10 [Radix-4 modular multiplication and exponentiation algorithms for the cryptosystem](#)

 Jin-Hua Hong, Cheng-Wen Wu


January 2000 **ASP-DAC '00: Proceedings of the 2000 conference on Asia South**

**Publisher:** ACM

Full text available:  Pdf (139.43 KB) Additional Information: [full citation](#), [references](#)

**Bibliometrics:** Downloads (6 Weeks): 3, Downloads (12 Months): 36, Citation (

- 11 [Data security for Web-based CAD](#)

 Scott Hauck, Stephen Knol

May 1998 **DAC '98: Proceedings of the 35th annual conference on Design**

**Publisher:** ACM

Full text available:  Pdf (198.79 KB)


Additional Information: [full citation](#), [abstract](#), [review](#)

**Bibliometrics:** Downloads (6 Weeks): 9, Downloads (12 Months): 38, Citation (

Internet-based computing has significant potential for improving most h including VLSI CAD. In this paper we consider the ramifications of the Ii and develop two models for Web-based CAD. We also ...

**Keywords:** Internet, Web-based CAD, data security, encryption

## 12 [New design concepts for an intelligent Internet](#)

 [Geng-Sheng Kuo, Jing-Pai Lin](#)

November 1998 **Communications of the ACM**, Volume 41 Issue 11


**Publisher:** ACM

Full text available:  Pdf (153.32 KB)

Additional Information: [full citation](#), [references](#)

**Bibliometrics:** Downloads (6 Weeks): 1, Downloads (12 Months): 30, Citation (

## 13 [Embedding SDL implemented protocols into DSP](#)

 [Antti Takko, Marko Hännikäinen, Jarno Knuutila, Timo Hämäinen, Jukka Sa](#)

November 2000 **CASES '00: Proceedings of the 2000 international conferen** and synthesis for embedded systems

**Publisher:** ACM


Full text available:  Pdf (370.17 KB)

Additional Information: [full citation](#), [references](#)

**Bibliometrics:** Downloads (6 Weeks): 0, Downloads (12 Months): 13, Citation (

**Keywords:** MAC protocol, SDL implementation, wireless LAN

## 14 [A methodology and algorithms for the design of hard real-time multit](#)

 [Miodrag Potkonjak, Wayne Wolf](#)

October 1999 **Transactions on Design Automation of Electronic Syste**

**Publisher:** ACM


Full text available:  Pdf (198.48 KB)

Additional Information: [full citation](#), [abstract](#), [review](#)

**Bibliometrics:** Downloads (6 Weeks): 12, Downloads (12 Months): 141, Citatio


Traditional high-level synthesis concentrates on the implementation of a controller, A/D converter). However, many applications—multifunctional intelligent wireless end-points, and DSP and multimedia ...

## 15 [A VLSI implementation of the blowfish encryption/decryption algorithm](#)

 [Michael C.-J. Lin, Youn-L. Lin](#)

January 2000 **ASP-DAC '00: Proceedings of the 2000 conference on Asia S**


**Publisher:** ACM

Full text available:  Pdf (53.76 KB)

Additional Information: [full citation](#), [references](#)

**Bibliometrics:** Downloads (6 Weeks): 10, Downloads (12 Months): 46, Citation

## 16 [Techniques for obtaining high performance in Java programs](#)

 [Ifat H. Kazi, Howard H. Chen, Berdenia Stanley, David J. Lilja](#)

September 2000 **Computing Surveys (CSUR)**, Volume 32 Issue 3

**Publisher:** ACM

Full text available:  Pdf (816.13 KB)


Additional Information: [full citation](#), [abstract](#), [re](#)

**Bibliometrics:** Downloads (6 Weeks): 55, Downloads (12 Months): 493, Citation

This survey describes research directions in techniques to improve the p  
written in the Java programming language. The standard technique for  
interpretation, which provides for extensive portability of programs. ...

**Keywords:** Java, Java virtual machine, bytecode-to-source translators,  
compilation, interpreters, just-in-time compilers

**17** [Design considerations and tools for low-voltage digital system design](#)

 [Anantha Chandrakasan, Isabel Yang, Carlin Vieri, Dimitri Antoniadis](#)

June 1996 **DAC '96: Proceedings of the 33rd annual conference on Design**

**Publisher:** ACM

Full text available:  Pdf (139.97 KB)

Additional Information: [full citation](#), [references](#), [cite](#)

**Bibliometrics:** Downloads (6 Weeks): 2, Downloads (12 Months): 21, Citation (

**18** [A fast implementation of DES and triple-DES on PA-RISC 2.0](#)

[Francisco Corella](#)

October 2000 **WIESS'00: Proceedings of the 1st conference on Indust**


**Systems Software - Volume 1** , Volume 1

**Publisher:** USENIX Association

Additional Information: [full citation](#), [references](#), [index terms](#)

**Bibliometrics:** Downloads (6 Weeks): n/a, Downloads (12 Months): n/a, Citation

**19** [Horus: a flexible group communication system](#)

 [Robbert van Renesse, Kenneth P. Birman, Silvano Maffei](#)

April 1996 **Communications of the ACM** , Volume 39 Issue 4


**Publisher:** ACM

Full text available:  Pdf (312.96 KB)

Additional Information: [full citation](#), [references](#), [cite](#)

**Bibliometrics:** Downloads (6 Weeks): 29, Downloads (12 Months): 236, Citation

**20** [CHIMAERA: a high-performance architecture with a tightly-coupled r](#)

 [Zhi Alex Ye, Andreas Moshovos, Scott Hauck, Prithviraj Banerjee](#)

June 2000 **ISCA '00: Proceedings of the 27th annual international sympos**

**Publisher:** ACM

Full text available:  Pdf (176.30 KB)

Additional Information: [full citation](#), [abstract](#), [re](#)

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Reconfigurable hardware has the potential for significant performance ir  
support for application-specific operations. We report our experience wi  
system that integrates a small and fast reconfigurable ...





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